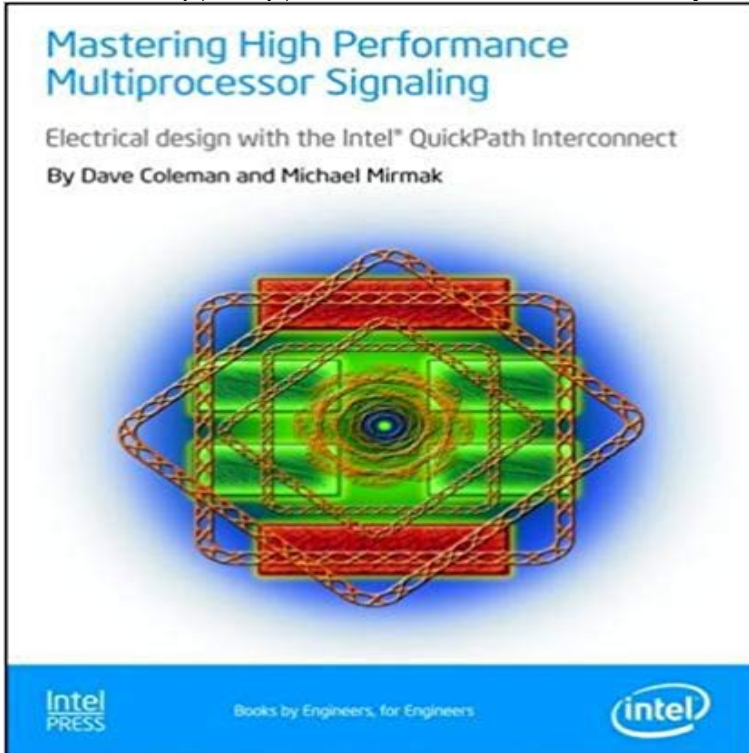


Mastering High Performance Multiprocessor Signaling



Mastering High Performance Multiprocessor Signaling explains the electrical design, board layout, test & measurement, and validation elements involved in implementing the Intel QuickPath Interconnect, the foundation of future generations of Intel microprocessor systems, using a high speed, packetized, point-to-point system interconnect that uses multiple narrow high speed differential links to stitch together processors into a fabric of a distributed shared memory-style platform architecture. Creating circuits for the very high speeds demanded by today's computers require skill sets that are not commonly provided by conventional electrical engineering education. Differential signaling is now the fundamental technology enabling high speed, microwave frequency data rates. Signaling speeds of Intel QuickPath Interconnect are now so high, transmission channel artifacts such as frequency dependent attenuation, ringing and crosstalk will have an influence across several bits of transmitted data. Customer Comments Mastering High Performance Multiprocessor Signaling nicely brings together an explanation of the various components of an Intel QuickPath Interconnect link: driver, channel, and receiver, and the trade-offs that need to be made to achieve a successful differential serial communications link.- Thomas Walley, Project Manager, Avago Technologies Mastering High Performance Multiprocessor Signaling provides a very good introduction to the principles of high speed differential serial circuit design. It quickly dives into how those principles specifically apply to designing with the Intel QuickPath Interconnect.- Aubrey Sparkman, SparkRight Solutions

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